

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Takashi WATANABE et al.**

Serial Number: **Not Yet Assigned**

Filed: **April 14, 2004**

For: **SEMICONDUCTOR DEVICE FABRICATION METHOD**

Attorney Docket No.: **042341**

Customer No.: **38834**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

April 14, 2004

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,
WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP



Scott M. Daniels
Reg. No. 32,562

1250 Connecticut Avenue, N.W., Suite 700
Washington, D.C. 20036
Tele: (202) 822-1100
Fax: (202) 822-1111
SMD/yap
Enclosures: PTO-1449; References (6)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 042341	Serial No. New Appln.
	Applicant(s): Takashi WATANABE et al.	
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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____ AE 11-104955	04/20/99	Japan	Abstract.
_____ AF 2000-248263	09/12/00	Japan	Abstract.
_____ AG 2001-9702	01/16/01	Japan	Abstract.
_____ AH 2001-85373	03/30/01	Japan	Abstract.
_____ AI 2001-338902	12/07/01	Japan	Abstract.
_____ AJ 2002-83787	03/22/02	Japan	Abstract.

OTHER DOCUMENTS

_____	AK	
Examiner	Date Considered	